

Substitute for form 1449/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet	1	of	3
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Complete if Known

Application Number	10/817,158
Filing Date	April 2, 2004
First Named Inventor	FASSIO, Marcelo Daniel Barú
Art Unit	3762
Examiner Name	MANUEL, George
Attorney Docket Number	99999-003368

U. S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

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Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶
		Country Code ³ ~ Number ⁴ ~ Kind Code ⁵ (if known)				
		CA 2,097,857	06-04-1993	Hoffer et al.		
		WO 2001/60445	08-23-2001	Neurodan A/S		
		WO 2002/13695	02-21-2002	Neopraxis PTY Ltd.		

Examiner
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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		ALVAREZ; BiCMOS Technology and Applications; 1993; pp. 317-318; 2nd Edition; Kulwer Academic Publishers.	
		ANDRES et al.; "How to Use Nerve Cuffs to Stimulate..."; Neural Prosthesis; 2001; pp. 139-175.	
		BINKLEY et al.; "A Micropower CMOS, Direct-Coverison..."; IEEE Journal of Solid-State Circuits; March 1998; pp. 344-358; vol. 33; issue 3.	
		DONALDSON et al.; "An Implantable Telemeter For Long-Term..."; Proceedings of the 5th Annual Conference of the International Functional Electrical Stimulation Society.	
		ENZ et al. ; "An Analytical MOS Transistor Model..."; Analog Integrated Circuit Signal Processing; 1995; pp. 83-114; vol. 8.	
		GRAY et al.; "MOS Operational Amplifier Design..."; IEEE Journal of Solid-State Circuits; 1982; pp. 969-982; vol. sc-17; issue 6.	
		GREGORIAN et al.; "Analog MOS Integrated Circuits for Signal Processing"; 1986; pp.131-133; John Wiley & Sons.	
		HAUGLAND et al.; "Slip Information Provided by Nerve Cuff..."; IEEE Transanctions on Rehabilitation Engineering; 1994; pp. 29-36; vo. 2; issue 1.	
		JINDAL; "Noise Associated with Distributed Resistance..."; IEEE Transactions on Electron Devices; 1984; pp. 1505-1509; vol 31; issue 10.	
		NICOLLINI et al.; "A 3,3-V 800-n rms Noise..."; IEEE Journal of Solid-State Circuits; 1998; pp. 915-921; vol. 28; issue 8.	

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Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		NIKOLIC et al.; "Instrumentation for ENG and..."; IEEE Transactions on Biomedical Engineering; 1994; pp. 703-706; vol. 41; issue 7.	
		PAPATHANASIOU et al.; "An Implantable CMOS Signal..."; IEEE International Symposium on Circuits and Systems; 2000.	
		SACKINGER et al.; "A General Relationship Between Amplifier Parameters..."; IEEE Transactions on Circuits and Systems; 1991; pp. 1173-1181; vol. 38; issue 10.	
		SACKINGER et al.; "A Versatile Building Block..."; IEEE Journal of Solid-State Circuits; 1987; pp. 287-294; vol. SC-22; issue 2.	
		SILVERIA et al.; "A gm-IsuBD Based Methodology..."; IEEE Journal of Solid-State Circuits; 1996; pp. 1314-1319; vol. 31; issue 9.	
		SINKJAER et al.; "Electroneurographic ENG Signals from ..."; Proceedings of the 5th Annual Conference of the International Functional Electrical Stimulation Society.	
		STRANGE et al.; "Gait Phase Information Provided..."; IEEE Transactions on Biomedical Engineering; 1999; pp. 797-809; vol. 46; issue 7.	
		VITTOZ; "Micropower Techniques, Design of Analog-Digital VLSI Circuits for Telecommunications..."; 1993; pp. 53-67.	

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